

Notice of References Cited	Application/Control No. 09/693,157	Applicant(s)/Patent Under Reexamination CHO, JANG-HO
	Examiner Shane F Gerstl	Art Unit 2183

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,055,629	04-2000	Kulkarni et al.	712/239
	B	US-6,484,256	11-2002	Levitin et al.	712/240
	C	US-5,564,118	10-1996	Steely et al.	712/240
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	International Conference on Parallel Architecture and Compilation Techniques, Branch Prediction Using Selective Branch Inversion, Srilatha Manne, Artur Klauser, and Dirk Grunwald, Oct 12-16, 1999
	V	Computer Organization and Design - The Hardware/Software interface, 2nd Edition, John L. Hennessy and David A. Patterson, Morgan Kaufman Publishers, 1998
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.